Google scholar ["state transition" "data access" read write cons] [Search] Advanced Scholar Search
Scholar Preferences

Scholar All articles Recent articles Results **1 - 10** of about **577** for **"state transition" "data access" read write consistency**. (0.15 seconds)

Replication, **consistency**, and practicality: are these mutually exclusive?
T Anderson, Y Breitbart, HF Korth, A Wool - Proceedings of the 1998 ACM SIGMOD international ..., 1998 - portal.acm.org
... Figure 1 depicts a **state-transition** diagram for the pro ... If TI submits a **read** or **write**
operation at ... In pro- cessing operations, maintain the **data access** set6 of ...
Cited by 122 - Related articles - BL Direct - All 4 versions

The MIT Alewife machine: A large-scale distributed memory multiprocessor - ► kfupm.edu.sa [PDF]
A Agarwal, D Chaiken, K Johnson, D Kranz, J ... - Scalable shared memory multiprocessors, 1991 - books.google.com
... P={kkk 12 n Figure 2: Directory **state transition** diagram. ... by one processor and then
read by all ... for every cycle it spends servicing an application **data access**. ...
Cited by 314 - Related articles - All 7 versions

An adaptive cache coherence protocol optimized for migratory sharing - ► kth.se [PDF]
P Stenström, M Brorsson, L Sandberg - ACM SIGARCH Computer Architecture News, 1993 - portal.acm.org
... the recommended mechanism to support shared **data access**. ... Last-writer pointer Figure
4: **State-transition** raph for ... The latter can reduce **read** and **write** penalty ...
Cited by 174 - Related articles - BL Direct - All 7 versions

MGS: a multigrain shared memory system - ► psu.edu [PDF]
D Yeung, J Kubiatowicz, A Agarwal - ACM SIGARCH Computer Architecture News, 1996 - portal.acm.org
... computing a cliff, and the **read-write** copy is ... consistent, or that is stronger than
release **consistency**. ... Figure 4 shows the **state transition** diagram for these ...
Cited by 97 - Related articles - BL Direct - All 16 versions

An effective on-chip preloading scheme to reduce **data access** penalty - ► brown.edu [PDF]
JL Baer, TF Chen - portal.acm.org
... very effective for reducing the **data access** penalty for ... **Write** buffers, stream
buffers[5], and lockup-free caches [6] are three mechanisms that allow the over- ...
Cited by 329 - Related articles - All 2 versions

Coordination for Internet application development
A Omicini, F Zambonelli - Autonomous Agents and Multi-agent systems, 1999 - Springer
... bound by the built-in **data-access** mechanisms provided ... to specifying a new **state**
transition in response ... ii) manipulate terms, and (iii) **read**, **write** and consume ...
Cited by 223 - Related articles - BL Direct - All 3 versions

[PDF] ► Effective hardware based data prefetching for high-performance processors
TF Chen, JL Baer - IEEE transactions on computers, 1995 - oss.pdx.edu
... basic scheme, Le., one without **state transition** mechanism. ... program analysis to detect
regular **data access** patterns ... prefetch instructions for both **read** and **write** ...
Cited by 346 - Related articles - View as HTML - BL Direct - All 27 versions

[PDF] ► Block-level **consistency** of replicated files
JL Carroll, DDE Long, JF Paris - Proc. of the 7th Int. Conf. on Distr. Computing Systems - cse.ucsc.edu
... executes the **consistency** control and **data access** algorithms. ... on the local site, then
the **read** operation can ... From the **state transition** diagram, we have kkpk = (n ...
Cited by 22 - Related articles - All 4 versions

[PDF] ► Integrated product and process management for engineering design applications
B Westfechtel - Integrated Computer Aided Engineering, 1996 - citeseer.comp.nus.edu.sg
... the CIM Manager itself cannot perform **consistency** control on ... Freeze Thaw **Read**, **Write**
Read ... Figure 6 **State transition** diagrams for revisions and configurations ...
Cited by 90 - Related articles - View as HTML - BL Direct - All 6 versions

Optimizing replication, communication, and capacity allocation in CMPs - ► snu.ac.kr [PDF]
Z Chishti, MD Powell, TN Vijaykumar - Computer Architecture, 2005. ISCA'05. Proceedings. ..., 2005 - ieeexplore.ieee.org
... CMP-NuRAPID (1) uses sequential tag-**data access**; (2) divides the ... Figure 4a shows
the **state transition** diagram for a 4 ... from I to C on a **read/write** miss requires ...
Cited by 90 - Related articles - BL Direct - All 32 versions

Goooooooooogle ►

Result Page: 1 2 3 4 5 6 7 8 9 10 **Next**

"state transition" "data access" read   Search